

HOD

S.D.M. College of Engineering and Technology, Dharwad

Department of Electronics and Communication Engineering

Academic Year: 2021-22

Class: IV Semester B.E.
 Division: A
 Hall No.: 26

TIME TABLE

Days	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30pm/5pm
MONDAY	18UECC403	18UECC404	BREAK	18UMAC400	18UECC401		BREAK	18UECC402	18UECC400
TUESDAY	18UECL405(A1) 18UECL406(A2)			18UECC401	18UECC402(T)	18UMAC400(T)		18UECC404	18UECC403
WEDNESDAY	18UECC400	18UECC402		18UECC403	18UMAC400			18UECC401	18UECC404
THURSDAY	18UECC404(T)	18UMAC400		18UECC400	18UECC402	18UECC403(T)		18UECL405(A4) 18UECL406(A3) 18UECL407	
FRIDAY	18UECC401(T)	18UECC400		18UECL405(A2) 18UECL406(A1)					
SATURDAY	18UECL405(A3) 18UECL406(A4)			18UECC401(T)	18UECC402(T)				

Note: TIME-TABLE IS EFFECTIVE FROM: 17-03-2022

S.No.	Course Code	Course Title	Course Credits	Course Instructor
1	18UMAC400	Engg. Mathematics-IV	3	Prof. Shreekanth Kanguri
2	18UECC400	Communication Systems - I	4	Prof. Vijaya C
3	18UECC401	Control Systems	4	Prof. M. Vijay Kumar
4	18UECC402	Microcontroller	4	Prof. Ravishankar S. S.
5	18UECC403	HDL Programming Using Verilog	3	Prof. Vinayak Miskin
6	18UECC404	Linear ICs and Applications	3	Prof. S. V. Viraktamath
7	18UECL405	HDL Programming Laboratory	1.5	Prof. Vinayak Miskin & Prof. Shrikanth K. Shirakol
8	18UECL406	Linear Integrated Circuits Laboratory	1.5	Prof. S. V. Viraktamath & Prof. Mala L. Muddannavar
9	18UECL407	Introductory Project	1	Prof. M. Vijay Kumar & Prof. Reshma Nadaf

Note: T—Tutorial Class

[Signature]
 HOD ECE 23/3/22

[Signature]
 Dean (Academic Program)

[Signature]
 Principal

S.D.M. College of Engineering and Technology, Dharwad
Department of Electronics and Communication Engineering

Class: IV Semester B.E.
 Division: B
 Hall No.: 27

TIME TABLE


Academic Year: 2021-22

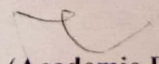
Days	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30pm/5pm
MONDAY	18UECC401(T)	18UECC403	BREAK	18UECL405(B1) 18UECL406(B2)		BREAK	18UECC400	18UECC402 (T)	
TUESDAY	18UECC400	18UECC402		18UMAC400	18UECC403		18UECC403(T)	18UECC401	18UECC404
WEDNESDAY	18UECC400	18UECC402		18UECL405(B3) 18UECL406(B4)			18UECC404	18UECC401(T)	
THURSDAY	18UMAC400(T)	18UECC401		18UECL405(B2) 18UECL406(B1)			18UECC404	18UECC402	
FRIDAY	18UECC402(T)	18UECC400		18UECC401	18UMAC400		18UECC404(T)	18UECL407	
SATURDAY	18UECC403	18UMAC400		18UECL405(B4) 18UECL406(B3)					

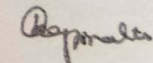
Note: TIME-TABLE IS EFFECTIVE FROM : 17-03-2022

S.No.	Course Code	Course Title	Course Credits	Course Instructor
1	18UMAC400	Engg. Mathematics-IV	3	Prof. Shreekanth Kanguri
2	18UECC400	Communication Systems - I	4	Prof. Sharada C. Sajjan
3	18UECC401	Control Systems	4	Prof. Savitri Raju
4	18UECC402	Microcontroller	4	Prof. Reshma Nadaf
5	18UECC403	HDL Programming Using Verilog	3	Prof. Shrikanth K. Shirakol
6	18UECC404	Linear ICs and Applications	3	Prof. Channakka C Lakkanavar
7	18UECL405	HDL Programming Laboratory	1.5	Prof. Vinayak Miskin & Prof. Shrikanth K. Shirakol
8	18UECL406	Linear Integrated Circuits Laboratory	1.5	Prof. S. V. Viraktamath & Prof. Mala L. Muddannavar
	18UECL407	Introductory Project	1	Prof. M. Vijay Kumar & Prof. Reshma Nadaf

Note: T—Tutorial Class


 HOD ECE 23/3/22


 Dean (Academic Program)


 Principal

S.D.M. College of Engineering and Technology, Dharwad
Department of Electronics and Communication Engineering

Class: VI Semester B.E.

TIME TABLE

Academic Year: 2021-22

Division: A

Hall No.: 25

Days	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30pm/5pm
MONDAY	18UECL602(A1) 18UECL603(A2)		BREAK	18UECC601	18UECO632	18UECE613	BREAK	18UECL604	
TUESDAY	18UECC600	18UECO632		18UECL602(A3) 18UECL603(A4)				18UECL604	
WEDNESDAY	18UECE613(T)	18UECC601		18UECE622	18UECC600	18UECO632		18UECL602(A2) 18UECL603(A1)	
THURSDAY	18UECC601	18UECE622		18UECC600	18UECE613				
FRIDAY	18UECL602(A4) 18UECL603(A3)			18UECE622	18UECC600				
SATURDAY	18UECE622(T)	18UECC601		18UECE613					

Note: TIME-TABLE IS EFFECTIVE FROM : 17-03-2022

S.No.	Course Code	Course Title	Course Credits	Course Instructor
1	18UECC600	Analog & Mixed Mode VLSI Design	4	Prof. Kalmeshwar. N. Hosur
2	18UECC601	IOT & Embedded System Design	4	Prof. Preeti S. Bellerimath
3	18UECE613	Operating System	3	Prof. Mala L. Muddannavar
4	18UECE622	Data structure using C++	3	Prof. Siddalingesh S. Navalgund
5	18UECO632	Automotive Electronics (Open Elective)	3	Prof. Vyas R Murnal
6	18UECL602	Embedded Laboratory	1.5	Prof. Jayashree C. Nidagundi & Prof. Kotresh E. Marali
7	18UECL603	VLSI Laboratory	1.5	Prof. Kalmeshwar. N. Hosur & Prof. Channakka C L.
8	18UECL604	Minor Project-2	2	Prof. Sumangala Bhavikatti & Prof. Channakka C Lakkanavar
9	18UHUL605	Soft skills/Aptitude	1	CIII

[Signature]
HOD ECE 23/3/22

[Signature]
Dean (Academic Program)

[Signature]
Principal

S.D.M. College of Engineering and Technology, Dharwad
Department of Electronics and Communication Engineering

Class: VI Semester B.E.

Division: B

Hall No.: 28

TIME TABLE

Academic Year: 2021-22

Days	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30pm/5pm	
MONDAY		18UECE622	BREAK	18UECC601	18UECO632	18UECC600	BREAK	18UECL602(B1) 18UECL603(B2)		
TUESDAY	18UECE613(T)	18UECO632		18UECE622	18UECC600				18UECL602(B3) 18UECL603(B4)	
WEDNESDAY	18UECL602(B2) 18UECL603(B1)			18UECE613	18UECC601	18UECO632			18UECL604	
THURSDAY	18UECL602(B4) 18UECL603(B3)			18UECE613	18UECE622				18UECL604	
FRIDAY	18UECE622(T)	18UECC600		18UECC601						
SATURDAY	18UECC601	18UECE613		18UECC600						

Note: TIME-TABLE IS EFFECTIVE FROM : 17-03-2022

S.No.	Course Code	Course Title	Course Credits	Course Instructor
1	18UECC600	Analog & Mixed Mode VLSI Design	4	Prof. Kalmeshwar. N. Hosur
2	18UECC601	IOT & Embedded System Design	4	Prof. Jayashree C. Nidagundi
3	18UECE613	Operating System	3	Prof. Sunil S. Mathad
4	18UECE622	Data structure using C++	3	Prof. Siddalingesh S. Navalgund
5	18UECO632	Automotive Electronics (Open Elective)	3	Prof. Raghuram K M
6	18UECL602	Embedded Laboratory	1.5	Prof. Jayashree C. Nidagundi & Prof. Kotresh E. Marali
7	18UECL603	VLSI Laboratory	1.5	Prof. Kalmeshwar. N. Hosur & Prof. Channakka C L.
8	18UECL604	Minor Project-2	2	Prof. Sumangala Bhavikatti & Prof. Channakka C Lakkanavar
9	18UHUL605	Soft skills/Aptitude	1	CIII

[Signature]
HOD ECE 23/3/22

[Signature]
Dean (Academic Program)

[Signature]
Principal

S.D.M. College of Engineering and Technology, Dharwad
Department of Electronics and Communication Engineering

Class: VIII Semester B.E.

Division: A

Hall No.: 31

TIME TABLE

Academic Year: 2021-22

Days	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30pm
MONDAY	18UECC800	18UECE811	BREAK		18UECE810(T)		BREAK	18UECL801(A1,A2)	
TUESDAY				18UECC800	18UECE811	18UECO820		18UECL801(A3,A4)	
WEDNESDAY	18UECE811(T)	18UECO820		18UECE810	18UECL802			18UECL802	
THURSDAY	18UECC800	18UECE811		18UECE810	18UECO820			18UECL802	
FRIDAY		18UECE810		18UECC800				18UECL802	
SATURDAY	18UECL802			18UECL802					

Note: i.) TIME-TABLE IS EFFECTIVE FROM : 17-03-2022

S.No.	Course Code	Course Title	Course Credits	Course Instructor
1	18UECC800	Wireless Communication	4	Prof. Shreedhar A. Joshi
2	18UECE810/ 18UECE811	Adhoc Wireless Networks/ Re-configurable Design	3	Prof. J V Sangeethagouda/ Prof. Satish S. Bhairannawar
3	18UECO820	DSP Architecture	3	Prof. Kotresh E. Marali
4	18UECL801	Technical Seminar	1	Prof. Vyas R Murnal & Prof. Sunil Mathad
5	18UECL802	Major Project Phase-2	7	Prof. Vyas R Murnal & Prof. Sunil Mathad

[Signature]
23/3/22
HOD ECE

[Signature]
Dean (Academic Program)

[Signature]
Principal

S.D.M. College of Engineering and Technology, Dharwad
Department of Electronics and Communication Engineering

Class: VIII Semester B.E.

Division: B

Hall No.: As indicated

TIME TABLE

Academic Year: 2021-22

Days	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30pm
MONDAY		18UECE810 (R. No. 25)/ 18UECE811 (R No. 31)	BREAK	18UECC800 (R. No.27)			BREAK	18UECL802	
TUESDAY		18UECC800 (R. No.26)			18UECE810/ (R. No.25) 18UECE811 (R. No.31)	18UECO820 (R. No.25)		18UECL802	
WEDNESDAY	18UECE810 (R. No.28)(T) / 18UECE811 (R. No.31)(T)	18UECO820 (R. No.28)		18UECC800 (R. No.27)				18UECL802	
THURSDAY		18UECE810 (R. No.28) / 18UECE811 (R. No.31)		18UECC800 (R. No.27)	18UECO820 (R. No. 27)			18UECL802	
FRIDAY	18UECL802			18UECL802				18UECL802	
SATURDAY				18UECL801(B1,B2) 18UECL801(B3,B4)					

Note: i.) TIME-TABLE IS EFFECTIVE FROM: 17.03.2022

S.No.	Course Code	Course Title	Course Credits	Course Instructor
1	18UECC800	Wireless Communication	4	Prof. Shreedhar A. Joshi
2	18UECE810/ 18UECE811	Adhoc Wireless Networks/ Re-configurable Design	3	Prof. J V Sangeethagouda/ Prof. Satish S. Bhairannawar
3	18UECO820	DSP Architecture	3	Prof. Kotresh E. Marali
4	18UECL801	Technical Seminar	1	Prof. Sunil Mathad & Prof. Vyas R Murnal&
5	18UECL802	Major Project Phase-2	7	Prof. Sunil Mathad & Prof. Vyas R Murnal&

[Signature]
23/3/22
HOD ECE

[Signature]
Dean (Academic Program)

[Signature]
Principal

S.D.M. College of Engineering and Technology, Dharwad
Department of Electronics and Communication Engineering

Class: PG-II SEM

TIME TABLE

Academic Year: 2021-22

Hall No.: PG ROOM 1

Days	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30pm
MONDAY		20PDEE252	BREAK	20PDEE255	20PDEC201		BREAK		
TUESDAY		20PDEC200		20PDEE252	20PDEE255				
WEDNESDAY		20PDEE255		20PDEC200	20PDEC201	20PDEE253			20PDEL202
THURSDAY		20PDEE252		20PDEE255	20PDEE253				
FRIDAY		20PDEC200		20PDEE253	20PDEE252	20PDEC201			20PDEL201
SATURDAY		20PDEC201		20PDEC200	20PDEE253				

Note: i.) TIME-TABLE IS EFFECTIVE FROM:

S.No.	Course Code	Course Title	Course Credits	Course Instructor
1	20PDEC200	Digital VLSI Design	4	Dr. Satish S. Bhairannawar
2	20PDEC201	Advanced Embedded System Design	4	Dr. S. S. Kerur
3	20PDEE252	Digital Signal Compression	4	Dr. Vijaya C
4	20PDEE253	Artificial Neural Networks and Deep Learning	4	Dr. Hemalata V. Bhujle
5	20PDEE255	Coding Theory	4	Dr. S. V. Viraktamath
6	20PDEL201	VLSI and Embedded Systems Laboratory	2	Dr. S. S. Kerur & Dr. Sunil Mathad
7	20PDEL202	Seminar	1	Dr. Vijaya C & Dr. Shreedhar A. Joshi

(Signature)
HOD ECE 23/3/22

(Signature)
Dean Academics

(Signature)
Principal

S.D.M. College of Engineering and Technology, Dharwad
Department of Electronics and Communication Engineering

Class: PG-IV SEM

TIME TABLE

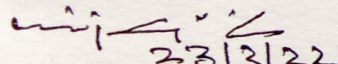
Academic Year: 2021-22

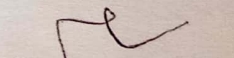
Hall No.: PG Room 2

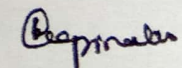
Days	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 4.30 pm
MONDAY	20PDEL400		BREAK	20PDEL400			BREAK	20PDEL400
TUESDAY	20PDEL400			20PDEL400				20PDEL400
WEDNESDAY	20PDEL400			20PDEL400				20PDEL400
THURSDAY								
FRIDAY								
SATURDAY								

Note: i.) TIME-TABLE IS EFFECTIVE FROM:

S.No.	Course Code	Course Title	Course Credits	Course Instructor
1	20PDEL400	Project Phase-II	22	Dr. Vijaya C Dr. Sharada C. Sajjan


 23/3/22
 HOD ECE


 Dean Academics


 Principal