

## MANDATORY DISCLOSURE OF INFORMATION

Note: Mandatory disclosure of information about accredited programmes by institutions to be included in their respective information brochure, displayed on their website and to be submitted to NBA every year well before the admission process

**Name of the Programme** : **Electronics and Communication Engineering**

**Approved Intake (Current)** :

Intake	Date of Approval	AICTE Letter No
120	15.05.2006	F.No.770-53-242(E)-RC/94.

**Fees charged (per annum) in Rs.** :

Category	Fees
Management	Rs,1,25,000
Government-Merit Quota	Rs.28,090

**Pass percentage during the last 5 years** :

Year	% of Passing			
	I/II Sem	III/IV Sem	V/VI Sem	VII/VIII Sem
2006-07	70.00	67.65	67.05	75.5
2005-06	91.7	46.26	50.34	94.26
	60.2	48.92	67.95	96.08
2004-05	91.7	69.68	54.33	95.11
	65.50	59.83	70.78	97.58
2003-04	80.43	66.41	65.08	86.66
	80.22	61.55	76.14	96.58
2002-03	85.79	57.94	67.43	96
	84.44	59.41	65.30	97.67

**Teacher Student Ratio** : **1:18**

**No. of Faculty Members : 26**

Cadre	Required As per AICTE	Existing			
		Permanent	Temporary	Guest	TOTAL
Professor	30	-			-
Associate Professor		-			-
Asst. Professors		09			9
Lecturers		17			17

**Faculty Profile** (Summary) :

Qualification	No. of Faculty		
	Completed	Pursuing	Total
Ph D	3	3	6
Post Graduate	12		12
Under Graduate	11		11

**Experience of Faculty** :

Experience	No. of Faculty
Less than 5 Years	18
5 to 10 Years	3
More than 10 Years	5

**Details of individual Faculty members** : Available at the end of this report- Annexure-A.

**Industrial Consultancy, Research and Development (during the past 5 years):**

Description of the Activity	Year ( 20xx )				
	07-08	06-07	05-06	04-05	03-04
Amount of Industrial Consultancy earned (in lakhs of Rupees)	----	----	----	----	----
No. of sponsored research projects completed	----	---	----	---	----
No. of patents produced	----	---	----	---	----
No. of sponsored research projects in progress	----	---	----	---	----
No. of papers published in International journals	07	3			
No. of papers published in National journals	03	1		1	1
No. of papers presented in International Conferences / Seminars	09	5	2	2	1
No. of papers presented in National Conferences /seminars	05	11	18	5	8
No. of International Conferences conducted	-	-	-	-	-
No. of National Conferences conducted	-	02	-	-	01
No. of Training Programmes / Short Term Courses conducted	04	3	3	-	-

**Placement Record for the previous year :**

Placement Details	Count
No. of Companies who visited the Campus	52
Names and Details of companies who visited the campus along with No. of Students placed	Enclosed Annexure B
No. of Students placed through campus recruitment	115
Maximum salary drawn (in Rs.) per annum	3,25,000.00
Average salary drawn (in Rs.) per annum	2,40,000.00

@ Details given at end of this Report- Annexure-B

**Budget Allocation for the Programme (in Rs.) :**

Year	Budget Allocation			
	Recurring		Non Recurring	
	Allocated	Spent	Allocated	Spent
2008-09	80,000.00	12,000.00	2,30,000.00	28,000.00
2007-08	60,000.00	10,000.00	1,40,000.00	Nil
2006-07	45,000.00	45,000.00	40,65,870.00	40,65,870.00
2005-06	80,000.00	80,000.00	43,66,161.00	33,76,161.00
2004-05	80,000.00	80,000.00	8,45,855.00	8,45,855.00
2003-04	48,000.00	48,000.00	10,13,578.00	9,65,578.00
2002-03	1,00,000.00	1,01,050.00	12,00,000.00	12,54,319.00

**New initiatives and future vision, if any :**

SL.No	New initiatives /Plan / Vision	Milestones Planned
1	E-CAD Computing Facilities	
2	VLSI Design Centre	
3	Digital Image Processing Lab.	
4	Embedded & Microcontroller Lab	

## MANDATORY DISCLOSURE OF INFORMATION

**Faculty Name** : Dr. Vijaya. C  
**Date of Birth** : 26-10-1966  
**Education Qualification** : B.E., M.Tech. Ph.D.



Degree	Degree Title	University/College Details	Grade Obtained	Remarks if any
Under Graduate	B.E.	Karnatak University Dharwad	First Class	
Post Graduate	M.Tech.	Mysore University Mysore.	First Class With Distinction	
Ph.D.	Ph.D.	Karnatak University Dharwad		

Work Experience:

Nature	No. of Years	Remarks if any
Teaching	16-Years	
Research	05 years	

Area of Specialization :

Sl. No	Area of Specialization
1	Digital Signal Processing
2	Image & Video processing

Subjects Handling :

Sl. No	Name of the Subject Handling Currently
1	Signals & Systems
2	Digital Signal Processing

Research Publications :

Sl. No	Details of Research Publications
1	"Signal compression using discrete fractional Fourier transform and set partitioning in hierarchical tree", Vijaya C and J. S. Bhat, Elsevier Publishing, Signal Processing, vol. 86, pp. 1976-1983, Aug. 2006
2	"Sliding Discrete Fractional Transforms", Vijaya C and J. S. Bhat, Elsevier Publishing, Signal Processing, available online from 22-07-2007.

Book Publications :

SI. No	Details of Book Publications
1	"Digital Signal Processing" Co Author Mr.S.Udayakumar S.

Papers Presented :

SI. No	Details of Papers Presented in International Conference
1	"Perceptually lossless Audio Codec based on DFRFT and SPIHT", J. S. Bhat and Vijaya C, Proc. of 13 <sup>th</sup> International Conference on ADCOM-2005, 14-17 <sup>th</sup> Dec. 2005, at Amrita Vishwa Vidyapeetham, Coimbatore, pp. 434.
2	"Block Edge Detection in Discrete Fractional Fourier Transform Domain", J. S. Bhat and Vijaya C, Proc. of IEEE International Conference on Signal and Image Processing, vol.2, 7-9 <sup>th</sup> Dec. 2006, at B.V.B. College of Engineering and Technology, Hubli, pp. 772-776.
	Paper Presented in National Conference:
1	"Signal compression based on DFRFT and SPIHT", J. S. Bhat and Vijaya C, Proc. of National Conference on ECCS-2004, 23-24 <sup>th</sup> Sept. 2004, at TIET, Patiala, pp. 187-190.):
2	"Audio Codec based on DFRCT", J. S. Bhat and Vijaya C, Proc. of NCC-2006, 27-29 <sup>th</sup> Jan. 2006, at IIT, New Delhi, pp.190-193.
3	"Implementation of DFRFT for Signal Compression", J. S. Bhat and Vijaya C, Proc. Of 13 <sup>th</sup> NCC-2007, 26-28 <sup>th</sup> Jan. 2007, at IIT Kanpur, pp. 103-106
4	Presented Research Paper on 'Signal Compression Based on DFRFT and SPIHT', in National Conference ECCS-2004, held at Patiala, 23 <sup>rd</sup> to 24 <sup>th</sup> Aug 2004.

Signature of the Employee



Faculty Name : **Dr.Girish V.Attimarad**

Date of Birth : **22-07-1970**

Education Qualification : **B.E., M.E. Ph.D.**

Degree	Degree Title	University/College Details	Grade Obtained	Remarks if any
Under Graduate	B.E.	Gulbarga University Gulbarga	First Class	
Post Graduate	M.E.	Karnatak University Dharwad.	First Class	
Ph.D.	Ph.D.	Delhi College of Engineering, Delhi University Delhi.		

Work Experience:

Nature	No. of Years	Remarks if any
Teaching	13	
Research	03	

Area of Specialization :

Sl. No	Area of Specialization
1	Wave Guides
2	Digital Electronics
3	Microwave Engineering

Subjects Handling :

Sl. No	Name of the Subject Handling Currently
1	Analog Communication

Research Guidance : **03**

Sl. No	Description of Research work
1	Design of circuits for communication applications using VHDL
2	Smart Antennas
3	Design of components using open wave guides

**Research Publications :**

Sl. No	Details of Research Publications
1	Asok De & Girish.V.Attimarad, Published a paper on " <b>Reflection Coefficient of Tapered Open Waveguide with Exponential and Triangular Distribution</b> " Abstracts, National Conference on Radio Science in India, NPL, New Delhi, Pg.No.15, 27 <sup>th</sup> -29 <sup>th</sup> November, 2003.
2	Asok De, Girish V Attimarad & J Sharma, " <b>Numerical Analysis of single step discontinuity in two dimensional Dielectric Waveguide</b> ", Journal of Electromagnetic Waves and Applications, Vol.17, No.6, 885-900, 2003.
3	Asok De & Girish.V.Attimarad, Published a paper on " <b>Design of Beam Expander; A Matching Section between Two Open Wave guides</b> ", Proceedings of International Conference on Communications Devices & Intelligent Systems (CODIS 2004)
4	Asok De & Girish V Attimarad, " <b>Numerical Analysis of Two Dimensional Tapered Dielectric Waveguide</b> ", Progress in Electromagnetic Research, PIER 44, 131-142, 2004.
5	Asok De & Girish V Attimarad, " <b>Numerical Analysis of Two Dimensional Tapered Dielectric Waveguide- Abstract</b> " Journal of Electromagnetic Waves and Applications, Vol.18, No.2,375-376, 2004.
6	Asok De & Girish.V.Attimarad, published a paper entitled " <b>Numerical Analysis of Two Dimensional Exponential Taper and Taper with Triangular Distribution in Open Waveguide</b> " and accepted for paper presentation at 36 <sup>th</sup> mid term symposium and exhibition "Emerging and Futuristic Communication Systems" April 29-May1, 2005 NSSC, IISC, Bangalore.4, 8 <sup>th</sup> to10th January 2004, Jadavpur University, Kolkata), 224-227.
7	Girish V Attimarad, " <b>Institute-Industry Interaction</b> ", An article published in a book Teaching & Techniques in Technical Education, 206-208, Anmol Publications Pvt., Ltd., New Delhi.

**Papers Presented :**

Sl. No	Details of Papers Presented
1	Asok De & Girish.V.Attimarad, Published a paper on " <b>Reflection Coefficient of Tapered Open Waveguide with Exponential and Triangular Distribution</b> " Abstracts, National Conference on Radio Science in India, NPL, New Delhi, Pg.No.15, 27 <sup>th</sup> -29 <sup>th</sup> November, 2003.
2	Asok De & Girish V.Attimarad Presented a paper entitled " <b>Scattering Parameters of Dielectric Waveguide</b> ", (NCC 2003) 9 <sup>th</sup> National Conference on Communications, held at IIT Madras, on January 31-2 <sup>nd</sup> February 2003.
3	Asok De, Girish V Attimarad & J Sharma, " <b>Numerical Analysis of single step discontinuity in two dimensional Dielectric Waveguide</b> ", Journal of Electromagnetic Waves and Applications, Vol.17, No.6, 885-900, 2003.
4	Asok De & Girish.V.Attimarad, Published a paper on " <b>Design of Beam Expander; A Matching Section between Two Open Wave guides</b> ", Proceedings of International Conference on Communications Devices & Intelligent Systems (CODIS 2004)
5	Asok De & Girish V Attimarad, " <b>Numerical Analysis of Two Dimensional Tapered Dielectric Waveguide</b> ", Progress in Electromagnetic Research, PIER 44, 131-142, 2004.

6	Asok De & Girish V Attimarad, “ <b>Numerical Analysis of Two Dimensional Tapered Dielectric Waveguide- Abstract</b> ” Journal of Electromagnetic Waves and Applications, Vol.18, No.2,375-376, 2004.
7	Asok De & Girish.V.Attimarad, published a paper entitled “ <b>Numerical Analysis of Two Dimensional Exponential Taper and Taper with Triangular Distribution in Open Waveguide</b> ” and accepted for paper presentation at 36 <sup>th</sup> mid term symposium and exhibition “Emerging and Futuristic Communication Systems” April 29-May1, 2005 NSSC, IISC, Bangalore.4, 8 <sup>th</sup> to10th January 2004, Jadavpur University, Kolkata), 224-227.
8	Girish V Attimarad, “ <b>Institute-Industry Interaction</b> ”, An article published in a book Teaching & Techniques in Technical Education, 206-208, Anmol Publications Pvt., Ltd., New Delhi.
9	Girish V Attimarad and R.S.Havinal “ <b>Smart Antenna for future Mobile/Wireless Communication presented</b> ” at National Conference on Converging Technology conducted at JCET Turkapally R.R District, Andhra Pradesh from 02-03-Feb 2007 .
10	Girish V Attimarad and R.S.Havinal “ <b>Interference reduction in Smart Antennas by Beam forming Technique</b> ” Presented at National conference on Information and communication Technology conducted at Narsee Monjee Institute of Management and Higher Studies (Deemed University) & DJ Sangvi college of Engineering Mumbai from 01-02-Mar 2007.
11	Girish V Attimarad and R.S.Havinal “ <b>Enhancing the network Throughput in Wireless adhoc network Using Smart Antenna</b> ” Presented at a National Conference on Advanced Computing and Computer Network on 9th -10 March 2007 at Dr Vithalrao Vikhe Patil College of Engineering Ahmednagar, Maharashtra Conference Proceedings page no 378-382
12	Girish V Attimarad & Rammana S Havinal, “ Enhancing the Throughput in Wireless Adhoc Network using Smart Antenna” National Conference on Advanced Computing and Computer Network, 378-382, 9 <sup>th</sup> -10 <sup>th</sup> March, Dr Vithalrao Vikhe Patil College of Engineering, Ahmednagar, Maharashtra, 2007.
13	Girish V Attimarad & Rammana S Havinal, “Performance improvement of Mobile Ad-hoc Networks using Smart Antenna”, National Conference on Emerging Trends in Signal Processing and Communication, pg no. 13-17, 27 <sup>th</sup> – 29 <sup>th</sup> December, Maharashtra Academy of Engineering, Alandi (D), Pune, 2007.
14	Girish V Attimarad & Rammana S Havinal, “Modelling of Communication Channels in View of Smart Antenna”, National Conference on Emerging Trends in Signal Processing and Communication, pg no. 18-23, 27 <sup>th</sup> – 29 <sup>th</sup> December, Maharashtra Academy of Engineering, Alandi (D), Pune, 2007.
15	Girish V Attimarad & Rammana S Havinal, “ <b>FPGA Techniques for Calibration abd Beam forming in Smart Antenna Applications</b> ” International conference on SPIT-IEEE Colloquium 2007 on Feb. 4 <sup>th</sup> & 5 <sup>th</sup> 2008 at Sardar Patel Institute of Technology, Mumbai
16	Dr.Girish V Attimarad & Rammana S Havinal, “ <b>Implementation of smart Antenna Adaptive Beamforming Algorithm in FPGA</b> ” International conference on SPIT-IEEE Colloquium 2007 on Feb. 4 <sup>th</sup> & 5 <sup>th</sup> 2008 at Sardar Patel Institute of Technology, Mumbai.
17	Dr.Girish V Attimarad & Rammana S Havinal, “ <b>Implementation of FPGA Signal Processing in software defined Radios</b> ” International conference on SPIT-IEEE Colloquium 2007 on Feb. 4 <sup>th</sup> & 5 <sup>th</sup> 2008 at Sardar Patel

Institute of Technology, Mumbai.
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**Signature of the Employee**



Faculty Name : **Mrs.Savitri Raju**

Date of Birth : **23-06-1970**

Education Qualification:**B.E., M.Tech., (Ph.D).**

<b>Degree</b>	<b>Degree Title</b>	<b>University/College Details</b>	<b>Grade Obtained</b>	<b>Remarks if any</b>
Under Graduate	B.E.	Karnatak University Dharwad	First Class	
Post Graduate	M.E	Karnatak University Dharwad	First Class Distinction	
Ph D.	(Ph.D.)	Registered for Ph. D in SDMCET Dharwad Research Centre.		

Work Experience :

<b>Nature</b>	<b>No. of Years</b>	<b>Remarks if any</b>
Teaching	13Years	

Area of Specialization :

<b>Sl. No</b>	<b>Area of Specialization</b>
1	Digital Signal Processing
2	Digital Control Systems

Subjects Handling :

<b>Sl. No</b>	<b>Name of the Subject Handling Currently</b>
1	<b>Cryptography</b>
2	Statistical Signal Processing

**Signature of the Employee**



Faculty Name : **Mr. Ravi.M.Yadahalli**

Date of Birth : 01-07-1967

Education Qualification : **B.E, M.Tech., (Ph.D)**

Degree	Degree Title	University/College Details	Grade Obtained	Remarks if any
Under Graduate	B.E.	Gulbarga University Gulbarga.	Second Class	
Post Graduate	M.Tech.	Gulbarga University Gulbarga	First Class	
PhD	(Ph.D)	Registered for Ph.D in Applied Electronics Department Gulbarga University Gulbarga.		Thesis Submitted

Work Experience:

Nature	No. of Years	Remarks if any
Teaching	15-Years	Including 03 years research
Research	03-Years	

Area of Specialization :

Sl. No	Area of Specialization
1	VLSI Design
2	Power Electronics

Subjects Handling :

Sl. No	Name of the Subject Handling Currently
1	Microwave and Radar

Research Guidance : **NIL**

Sl. No	Description of Research work
1	“A Study of Some Aspects of Compact Microstrip Antennas”

Research Publications :

Sl. No	Details of Research Publications
1	Usha Kiran K., <b>Ravi M. Yadahalli</b> , R. M. Vani, Sara F. Farida and P. V. Hunagund“Microstrip-line-fed rectangular microstrip antenna with open end meandering slots in the ground plane for compact broadband operation”,

	<b>Microwave and Optical Technology Letters</b> , Vol. 49, No. 4, pp. 824-827, April 2007.
2	Usha Kiran K., <b>Ravi M. Yadahalli</b> , R. M. Vani, Sara F. Farida and P. V. Hunagund "Compact L-shaped printed antennas with dual slits for multiband and wideband Applications", <b>Microwave and Optical Technology Letters</b> , Vol. 49, No. 8, pp. 2022-2026, 2007.
3	Usha Kiran K., <b>Ravi M. Yadahalli</b> , R. M. Vani, Sara F. Farida and P. V. Hunagund, "Compact broadband dual-frequency slot-loaded rectangular microstrip antenna with ground plane meandering slots and stacking", <b>Microwave and Optical Technology Letters</b> , Vol. 49, No. 10, pp. 2477-2481, 2007.
4	Usha Kiran K, <b>Ravi M Yadahalli</b> , R M Vani and P V Hunagund, "Effect of various meandering slots in the rectangular microstrip antenna ground plane for compact broadband operation", <b>Electronics Letters (UK)</b> , Vol. 43, No.16, pp.848-850, Aug.2007.
5	<b>Ravi M. Yadahalli</b> , Usha Kiran K., N. H. Ayachit, R. M. Vani, N. H. Ayachit and P.V. Hunagund "Broadband compact stacked c-slot square microstrip antenna", <b>Asian Journal of Physics</b> (Communicated).
6	<b>Ravi M. Yadahalli</b> , Usha Kiran K., N. H. Ayachit, R. M. Vani, N. H. Ayachit and P.V. Hunagund "Compact dual-frequency microstrip patch antenna using a pair of meandered $\Pi$ -shaped slots", <b>Asian Journal of Physics</b> (Communicated).
<b>National Journals:</b>	
1	Usha Kiran K., <b>Ravi M. Yadahalli</b> , R. M. Vani, S. Nirate and P. V. Hunagund, "Compact broadband dual wide slit loaded rectangular microstrip antenna", <b>Indian Journal of Radio &amp; Space Physics</b> . (Communicated)
2	Usha Kiran K., <b>R. M. Yadahalli</b> , Vani R. M. and P. V. Hunagund, "Bandwidth enhancement of the compact ground plane meandered microstrip antenna using microstripline feed and stacking techniques", <b>Journal of Institution of Engineers</b> .(Communicated)
3	<b>Ravi M. Yadahalli</b> , Usha Kiran K., R. M. Vani, N. H. Ayachit and P. V. Hunagund "A study of stacked slotted rectangular microstrip antenna for T & $\Pi$ slots for compact operation", <b>Icfai Journal of Electrical &amp; Electronics Engineering</b> .(Communicated).
<b>PUBLICATIONS IN CONFERENCES &amp; SYMPOSIUMS (International conferences and symposium)</b>	
1	<b>Ravi M. Yadahalli</b> , Usha Kiran K., N. H. Ayachit, R. M. Vani, Sara F. Farida and P. V. Hunagund "Effect of U-slot length variations on the performance of stacked microstrip antenna", AIP ( <b>American Institute of Physics</b> ) conference proceedings, Melville, New York, 2007, Volume 923, Pages 263-268.
2	<b>Ravi M. Yadahalli</b> , Vani R. M, S.F.Farida, and P. V. Hunagund, "Compact Stacked Microstrip Antenna with Inverted C-slot for wireless communications", <b>Progress In Electromagnetics Symposium (PIERS 2006)</b> , Tokyo, Japan, 2-5 August 2006, pp-594.
3	<b>Ravi M. Yadahalli</b> , Vani R. M, S.F.Farida, and P. V. Hunagund, "Enhancement of Bandwidth of Compact Stacked Microstrip Antenna using Air gap Technique" <b>Progress In Electromagnetics Symposium (PIERS 2006)</b> , Tokyo, Japan, 2-5 August 2006, pp-595.
4	<b>Ravi M. Yadahalli</b> , Vani R. M, S. F. Farida, and P. V. Hunagund, "Slot Loaded Equilateral Triangular Microstrip Antenna" <b>Progress In ElectromagneticsSymposium (PIERS 2006)</b> , Tokyo, Japan, 2-5 August

	2006, pp-596.
5	<b>Ravi M. Yadahalli</b> , Vani R. M., S.F.Farida, and P. V. Hunagund, "Small single Cslot Stacked Microstrip Patch Antenna", <b><i>International Conference on Advanced Technologies In Telecommunications and Control Engineering (ATTCE, 2006)</i></b> , 28-29 <sup>th</sup> August, Malaysia 2006.
6	Usha Kiran K., <b>Ravi M. Yadahalli</b> , R. M. Vani and P. V. Hunagund, "Slot-loaded rectangular microstrip antenna with meandering slots in the ground plane for compact broadband dual-frequency operation", <b><i>Proc. of 7<sup>th</sup> International Symposium on Antennas, Propagation and EM Theory (ISAPE-2006)</i></b> , Oct. 26-29, Guilin, China, 2006.
7	Usha Kiran K., <b>Ravi M. Yadahalli</b> , R. M. Vani and P. V. Hunagund, "Compact rectangular microstrip antenna with open end meandering slots in the ground plane for bandwidth enhancement", <b><i>Proc. of International Symposium on Antennas and Propagation (ISAP-2006)</i></b> , Nov. 1-4, Singapore, p. 271, 2006.
8	Usha Kiran K., Jyoti G., <b>Ravi M. Yadahalli</b> , R. M. Vani and P. V. Hunagund, "Center-fed rectangular microstrip antenna with meandering slots in the ground plane for compact broadband operation", <b><i>IEEE - International Symposium on Microwaves (IEEE - ISM 06)</i></b> , Dec.15-17, Bangalore, India, pp. 770-776, 2006.
9	<b>Ravi M. Yadahalli</b> , Usha Kiran K., Vani R. M., N. H. Ayachit, S. F. Farida and P. V. Hunagund, "Effect of U-slot length variations on the performance of the Stacked microstrip antenna", <b><i>International Symposium on Rain Fall Rate and Radio Wave Propagation (ISRR-2007)</i></b> , Sona College of Technology, Salem, 29 <sup>th</sup> 30 <sup>th</sup> January 2007-08
10	<b>Ravi M. Yadahalli</b> , Usha Kiran K., Vani R. M., N. H. Ayachit, S. F. Farida and P. V. Hunagund, "Effect of U-slot width variations on the performance of the Stacked microstrip antenna", <b><i>International Conference on Liquid Crystals ICLC (ICLC-2006)</i></b> ,. University of Mumbai, Mumbai, December 5-8, 2006.
11	Usha Kiran K., <b>Ravi M. Yadahalli</b> , R. M. Vani and P. V. Hunagund, "Small size broadband L-shaped notched printed antenna with dual slits", <b><i>2007 IEEE International Symposium on Antennas and Propagation (IEEE AP-S)</i></b> , June 10-15, Honolulu, HI, USA, 2007.
12	<b>Ravi M. Yadahalli</b> , Usha Kiran K., Vani R. M, S. F. Farida, and P. V. Hunagund, 070321095740 "Compact 1B2T microstrip antenna with meandered slots", <b><i>Progress In Electromagnetics Symposium (PIERS 2007)</i></b> , August 27–30, 2007 Prague, Czech Republic.
13	<b>Ravi M. Yadahalli</b> , Usha Kiran K., Vani R. M, S. F. Farida, and P. V. Hunagund, 070321095520 "A study of compact stacked microstrip antenna", <b><i>Progress In Electromagnetics Symposium (PIERS 2007)</i></b> , August 27–30, 2007 Prague, Czech Republic.
14	Usha Kiran K., <b>Ravi M. Yadahalli</b> , R. M. Vani and P. V. Hunagund, "Compact printed L-shaped notched antenna with dual slits for 2.4/5 GHz WLAN", <b><i>IEEE International Conf. &amp; Exhibition on Advances in Electrical, Electronics, Communication and Information Technology, Indicon 2007 &amp; 16<sup>th</sup> Annual Symp. IEEE Bangalore section, 2007.</i></b> (Communicated)
<b>II. National conferences and symposium:</b>	
1	<b>Ravi M. Yadahalli</b> , Usha Kiran K., Vani R.M., S. F. Farida and P. V. Hunagund, "Study of Size reduction on the performance of the stacked microstrip antenna", <b><i>National Society of Instrumentation NSI- 30, CUSAT, Cochin.</i></b>

2	Usha Kiran K., <b>Ravi M. Yadahalli</b> , R. M. Vani, and P. V. Hunagund, "Dual wide slits microstrip antenna with a pair of asymmetric right-angle slots for broadband and compact broadband applications", <b><i>Proc. of National Conf. on Emerging Trends in Physics and Engineering Sciences 2006 (ETPEE-06)</i></b> , Sept. 25-26 Mysore, India, pp. 400-404, 2006
3	<b>Ravi M. Yadahalli</b> , Usha Kiran K., R. M. Vani, S. F. Farida and P. V. Hunagund, "Compact Stacked Microstrip antenna with gap coupling", <b><i>Proc. of National Conf. on Emerging Trends in Physics and Engineering Sciences 2006 (ETPEE-06)</i></b> , Sept. 25-26 Mysore, India, pp. 404-407, 2006.
4	<b>Ravi M. Yadahalli</b> , Usha Kiran K., R. M. Vani, S. F. Farida and P. V. Hunagund, "Study of size reduction on the performance of the stacked microstrip antenna", <b><i>National symposium on Instrumentation (NSI-31)</i></b> , 12-15th Oct., ITM Universe, Gwalior, Madhya Pradesh, pp. 133, 2006.
5	<b>Ravi M. Yadahalli</b> , Usha Kiran K., R. M. Vani, S. F. Farida and P. V. Hunagund, "Comparative study of Compact stacked microstrip Antennas", <b><i>National Conference on Recent Advances in Electronics and Communication Technology (RAECT 2006)</i></b> , November 9- 10 2006, Guru Nanak Dev College of Engineering, Ludhiana.

**Signature of the Employee**



**A.**  
Faculty Name : **Mr.Siddalingesh S.Kerur**

Date of Birth : **01-10-1971**

Education Qualification : **B.E., M.E. (Ph.D)**

Degree	Degree Title	University/College Details	Grade Obtained	Remarks if any
Under Graduate	B.E.	Basaveshwar Engineering College Bagalkot	Second Class	
Post Graduate	M.E.	Karnatak University Dharwad	First Class	
PhD	(Ph.D)	VTU ,Registered for Ph.D. in SDM CET Dharwad Research Centre		Course work is completed

Work Experience:

Nature	No. of Years	Remarks if any
Teaching	12-Years	

Area of Specialization :

Sl. No	Area of Specialization
1	VLSI Design
2	Lowpower VLSI

Subjects Handling :

Sl. No	Name of the Subject Handling Currently
1	Basic Electronics
2	VLSI Circuits

Research Guidance : **NIL**

Sl. No	Description of Research work
1	Digital VLSI Design

Research Publications :

Sl. No	Details of Research Publications
1	Paper entitled " <b>Calculation of Euclidean Distance for QAM Applications</b> " Published in the VLSI India journal.Vol.1, Issue 2 Dec 2005.

Papers Presented :

Sl. No	Details of Papers Presented
1	Paper entitled for " <b>Low power chip design for communication systems</b> " published in the proceedings of national conference held in RVCE in Bangalore on 21-22 July 2005. and got best paper award.
2	Paper entitled " <b>Mathematical Modeling and VLSI design issues</b> " Published in the proceedings of national conference held in PSGCT Coimbotore on 15-16 Dec 2005.

**Signature of the Employee**



Faculty Name : **Mr. Kalmeshwar .N.Hosur**

Date of Birth : **09-07-1975**

Education Qualification : **B.E. M.Tech.**

Degree	Degree Title	University/College Details	Grade Obtained	Remarks if any
Under Graduate	B.E.	Karnataka University, Dharwad	First Class	
Post Graduate	M.Tech.	Visveswaraya Technological University, Belgaum	FCD	

Work Experience :

Nature	No. of Years	Remarks if any
Teaching	9-Years	

Area of Specialization :

Sl. No	Area of Specialization
1	Electronics and Communication
2	VLSI Circuits Design & Embedded Systems

Subjects Handling :

Sl. No	Name of the Subject Handling Currently
1	Analog Electronics
2	CMOS VLSI Design

**Signature of the Employee**



Faculty Name : **Mr. S.V. Viraktamath**

Date of Birth : **08-09-1973**

Education Qualification : **B.E., M.Tech,**

Degree	Degree Title	University/College Details	Grade Obtained	Remarks if any
Under Graduate	B.E.	Baseveshwar Engineering College Bagalokot	First Class With Distinction	
Post Graduate	M.Tech	Visveswaraya Technological University, Belgaum	First Class With Distinction	First rank to VTU

Undergone twelve years of technical education.

Work Experience :

Nature	No. of Years	Remarks if any
Teaching	12-Years	
Industrial	08-Months	

Area of Specialization :

Sl. No	Area of Specialization
1	Communication Engineering
2	Computer Networking
3	Microprocessor

Subjects Handling :

Sl. No	Name of the Subject Handling Currently
1	Operating Systems

**Signature of the Employee**



Faculty Name : **Mrs. Jayashree C. Nidagundi**

Date of Birth : **20-01-1974**

Education Qualification : **B.E., M.Tech,**

Degree	Degree Title	University/College Details	Grade Obtained	Remarks if any
Under Graduate	B.E.	Karnatak University, Dharwad	First Class	
Post Graduate	M.Tech.	Visveswaraya Technological University, Belgaum	First Class with Distinction	

**Work Experience :**

Nature	No. of Years	Remarks if any
Teaching	6-Years	

**Area of Specialization :**

Sl. No	Area of Specialization
1	VLSI Design
2	Embedded systems

**Subjects Handling :**

Sl. No	Name of the Subject Handling Currently
1	VLSI Circuits
2	Optical Communication & Networking

**Signature of the Employee**



Faculty Name : **Mrs. Sharada.C.Sajjan**

Date of Birth : **30-06-1971**

Education Qualification : **B.E.,M.E.**

Degree	Degree Title	University/College Details	Grade Obtained	Remarks if any
Under Graduate	B.E.	Karnatak University, Dharwad	First Class	
Post Graduate	M.E.	Karnatak University, Dharwad	First Class with Distinction	

Work Experience:

Nature	No. of Years	Remarks if any
Teaching	112Years	

Area of Specialization :

Sl. No	Area of Specialization
1	Digital Signal Processing
2	Communication Systems

Subjects Handling :

Sl. No	Name of the Subject Handling Currently
1	Signals and Systems
2	Digital Signal Processing

Papers Presented :

Sl. No	Details of Papers Presented
1	Presented paper titled 'Speech Recognition using HMM', at NCSPC- 2006, JNNCE, Shimoga, Jul. 7-8 <sup>th</sup> 2006.

**Signature of the Employee**

**A.**

Faculty Name : **Mrs. Renuka H.Korti**

Date of Birth : **28-10-1976**

Education Qualification : **B.E.,M.Tech.**



Degree	Degree Title	University/College Details	Grade Obtained	Remarks if any
Under Graduate	B.E.	Karnatak University Dharwad.	First Class	
Post Graduate	M.Tech.	Visveswaraya Technological University, Belgaum	First Class with Distinction	Secured III Rank VTU

Work Experience :

Nature	No. of Years	Remarks if any
Teaching	8-Years	

Area of Specialization :

Sl. No	Area of Specialization
1	Microprocessor
2	Advanced Communication
3	Cryptography
4	Digital Circuits and Logic Design

Subjects Handling :

Sl. No	Name of the Subject Handling Currently
1	Digital Circuit and Logic Design
2	Cryptography

Awards :

Sl. No	Details of Awards
1	Secured 3 <sup>rd</sup> rank in M.Tech. Digital Electronics Visveswaraya Technological University, Belgaum during the year 2001.

**Signature of the Employee**

Faculty Name : **Mr.Siddalingesh S.Navalgund**

Date of Birth : **18-07-1978**

Education Qualification : **B.E. M.Tech. (Ph.D)**



Degree	Degree Title	University/College Details	Grade Obtained	Remarks if any
Under Graduate	B.E.	Karnataka University, Dharwad.	First Class with Distinction	
Post Graduate	M.Tech.	Visveswaraya Technological University, Belgaum	First Class with Distinction	
PhD	-	VTU, Registered for Ph.D in SDMCET Dharwad Research Centre		Completed coursework

Work Experience :

Nature	No. of Years	Remarks if any
Teaching	6 Years	

Area of Specialization :

Sl. No	Area of Specialization
1	VLSI
2	Signals & Systems
3	Embedded Systems Programming

Subjects Handling :

Sl. No	Name of the Subject Handling Currently
1	Entrepreneurship Development & Management

Research Publications :

Sl. No	Details of Research Publications
1	Presented a paper entitled " <b>FIR Filter Design and FPGA Implementation</b> " at National Conference on Signals, Systems and Security (NCSSS 2006) held at Bannari Amman Institute of Technology, Sathyamangalam, TamilNadu during 24 <sup>th</sup> & 25 <sup>th</sup> Feb. 2006.

Papers Presented :

Sl. No	Details of Papers Presented
1	Presented a paper entitled " <b>MBD-DC Algorithm</b> " at state level conference PG FEST at NMAMIT, Nitte, during July 2003.

**Signature of the Employee**

**A.**

Faculty Name : **Mrs. Mala. L.Muddannavar**



Date of Birth : **01-03-1964**

Education Qualification : **B.E., M.E.**

Degree	Degree Title	University/College Details	Grade Obtained	Remarks if any
Under Graduate	B.E.	Mysore University Mysore.	Second Class	
Post Graduate	M.E.	Gulbarga University, Gulbarga	First Class with Distinction	

Work Experience :

Nature	No. of Years	Remarks if any
Teaching	8-Years	

Area of Specialization :

Sl. No	Area of Specialization
1	Digital Electronics
2	Microprocessor

Subjects Handling :

Sl. No	Name of the Subject Handling Currently
1	Linear IC's & Applications

**Signature of the Employee**

**A.**



**Faculty Name** : Mr. Vinayak P. Miskin

**Date of Birth** : 22-05-1981

**Education Qualification** : **B.E., M.Tech,**

Degree	Degree Title	University/College Details	Grade Obtained	Remarks if any
Under Graduate	B.E. 2004	VTU, Belgaum	FC	Electrical & Electronics
Post Graduate	M.Tech 2006	VTU, Belgaum	FC	Microelectronics & Control Systems
PhD	-	-		
Others if any	-	-		

**Work Experience** :

Nature	No. of Years	Remarks if any
Teaching	03	
Research	-	
Others	01	Worked as a Design and Verification Engineer in VLSI ( M/s. Silicon Interfaces Pvt. Ltd., Mumbai)
Others if any	-	

**Area of Specialization** :

Sl. No	Area of Specialization
1	Digital Electronics
2	Electronics & Communication

**Subjects Handling** :

Sl. No	Name of the Subject Handling Currently
1	Basic Electronics

**Signature of the Employee**

**A.**

**Faculty Name** : Mr. Mahendra.M.Dixit

**Date of Birth** : 12-05-1982

**Education Qualification** : **B.E. (E&CE)**



Degree	Degree Title	University/College Details	Grade Obtained	Remarks if any
Under Graduate	B.E. 2007	KLE, Societies College of Engineering & Technology, Belgaum	FC	Electronics & Communication
Post Graduate	-	-	-	-
PhD	-	-	-	-
Others if any	-	-	-	-

**Work Experience** :

Nature	No. of Years	Remarks if any
Teaching	01	-
Research	-	-
Others	-	-
Others if any	-	-

**Area of Specialization** : **NIL**

Sl. No	Area of Specialization
1	Electronics and Communication

**Subjects Handling** :

Sl. No	Name of the Subject Handling Currently
1	Microcontroller & Applications

**Signature of the Employee**

**A.**

**Faculty Name** : Mrs. Sumangala Bhavikatti



**Date of Birth** : 18-01-1979

**Education Qualification** : **B.E. (E&CE)**

Degree	Degree Title	University/College Details	Grade Obtained	Remarks if any
Under Graduate	B.E. 2001	REC,Hulkoti	FC	Electronics & Communication
Post Graduate	M.Tech	BVB,College of Engineering & Technology, Hubli		Pursuing
PhD	-	-		
Others if any	-	-		

**Work Experience** :

Nature	No. of Years	Remarks if any
Teaching	4.5	
Research	-	-
Others	-	-
Others if any	-	

**Area of Specialization** :

Sl. No	Area of Specialization
1	Digital Electronics

**Subjects Handling** :

Sl. No	Name of the Subject Handling Currently
1	Computer Communication Networks

**Signature of the Employee**



**A.**  
**Faculty Name** : Ms. Reshma. Nadaf

**Date of Birth** : 27-05-1985

**Education Qualification** : B.E.(E&E)

Degree	Degree Title	University/College Details	Grade Obtained	Remarks if any
Under Graduate	B.E. 2004	STJIT, Ranebennur / VTU, Belgaum	FC	Electrical & Electronics
Post Graduate	-	-	-	-
PhD	-	-	-	-
Others if any	-	-	-	-

**Work Experience** :

Nature	No. of Years	Remarks if any
Teaching	2-years	
Research	-	-
Others	01	Worked as Executive HR in Copper-IT, Bangalore.
Others if any	-	

**Area of Specialization** :

Sl. No	Area of Specialization
1	Electrical & Electronics

**Subjects Handling** :

Sl. No	Name of the Subject Handling Currently
1	Digital Circuit Design

**Signature of the Employee**

**A**



**A.**  
**Faculty Name** : Mr.Hareesh S.Katti

**Date of Birth** : 14-06-1985

**Education Qualification** : **B.E.(E&E)**

Degree	Degree Title	University/College Details	Grade Obtained	Remarks if any
Under Graduate	2008	SDM College of Engineering and Technology, Dharwad	FCD	Electronics & Communication
Post Graduate	-	-	-	-
PhD	-	-	-	-
Others if any	-	-	-	-

**Work Experience** : Nil

Nature	No. of Years	Remarks if any
Teaching	-	-
Research	-	-
Others	-	-
Others if any	-	-

**Area of Specialization** :

Sl. No	Area of Specialization
1	Electronics and Communication

**Subjects Handling** :

Sl. No	Name of the Subject Handling Currently
1	Analog Electronics

**Signature of the Employee**



**Faculty Name** : Ms. Preeti S. Bellerimath

**Date of Birth** : 20-08-1986

**Education Qualification** : B.E.(E&C)

Degree	Degree Title	University/College Details	Grade Obtained	Remarks if any
Under Graduate	B.E. 2008	SDM College of Engineering and Technology, Dharwad	FCD	Electronics & Communication
Post Graduate	-	-	-	-
PhD	-	-	-	-
Others if any	-	-	-	-

**Work Experience** :NIL

Nature	No. of Years	Remarks if any
Teaching	-	-
Research	-	-
Others	-	-
Others if any	-	-

**Area of Specialization** :

Sl. No	Area of Specialization
1	Electronics and Communication
2	-

**Subjects Handling** :

Sl. No	Name of the Subject Handling Currently
1	Logic Design

**Signature of the Employee**

Faculty Name : Abhishekh Hegde  
 Date of Birth : 11.12.1986  
 Education Qualification : BE.

Degree	Degree Title	University/College Details	Grade Obtained	Remarks if any
Under Graduate	B.E	SDMCET, Dharwad	FCD	
Post Graduate				
PhD				
Others if any				

Work Experience :

Nature	No. of Years	Remarks if any
Teaching	0.04	
Research		

Area of Specialization :

Sl. No	Area of Specialization
1	
2	

Subjects Handling :

Sl. No	Name of the Subject Handling Currently
1	

**Signature of the Employee**

**Faculty Name** : Sumeet.

Date of Birth :

**Education Qualification** :

Degree	Degree Title	University/College Details	Grade Obtained	Remarks if any
Under Graduate				
Post Graduate				
PhD				
Others if any				

Work Experience :

Nature	No. of Years	Remarks if any
Teaching		
Research		
Others		
Others if any		

Area of Specialization :

Sl. No	Area of Specialization
1	
2	

Subjects Handling :

Sl. No	Name of the Subject Handling Currently
1	

**Signature of the Employee**

**Faculty Name** : Shwetha P T

Date of Birth :

**Education Qualification** :

Degree	Degree Title	University/College Details	Grade Obtained	Remarks if any
Under Graduate				
Post Graduate				
PhD				
Others if any				

Work Experience :

Nature	No. of Years	Remarks if any
Teaching		
Research		
Others		
Others if any		

Area of Specialization :

Sl. No	Area of Specialization
1	
2	

Subjects Handling :

Sl. No	Name of the Subject Handling Currently
1	

**Signature of the Employee**

**Faculty Name** : Shwetha M Kandakur

Date of Birth :

**Education Qualification** :

Degree	Degree Title	University/College Details	Grade Obtained	Remarks if any
Under Graduate				
Post Graduate				
PhD				
Others if any				

Work Experience :

Nature	No. of Years	Remarks if any
Teaching		
Research		
Others		
Others if any		

Area of Specialization :

Sl. No	Area of Specialization
1	
2	

Subjects Handling :

Sl. No	Name of the Subject Handling Currently
1	

**Signature of the Employee**

**Faculty Name** : Raghuram K M  
**Date of Birth** : 26.05.87  
**Education Qualification** : B.E.

Degree	Degree Title	University/College Details	Grade Obtained	Remarks if any
Under Graduate	B.E	SDMCET, Dharwad	79.06 %	

Work Experience : 2 Years.

Nature	No. of Years	Remarks if any
Teaching	2	
Research		

Area of Specialization :

Sl. No	Area of Specialization
1	
2	

Subjects Handling :

Sl. No	Name of the Subject Handling Currently
1	

**Signature of the Employee**

Annexure-B: Details of Companies visited last Year

**August 2008 batch students**

SL. No.	Name & Details of the Company	No of	Salary Offered per
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		Students Recruited	Annum in Rs	
			Max	Min
1	Mindtree Consulting	7	3,25,000.00 Per annum	2,40,000.00 Per annum
2	Infosys	2		
3	Caritor	14		
4	CTS	22		
5	iGATE	1		
6	Wipro Technologies	26		
7	TATA Elxsi	3		
8	L&T Infotech	2		
9	Mphasis Tech Ltd	2		
10	KPIT Cummins Infosystems Pune	2		
11	Sonata Software	3		
12	Acuvate	5		
13	Siemens Technologies	2		
14	IBM	3		
15	Accenture	7		
16	Wipro BPO	4		
17	T-systems, Pune	1		
18	Vayavya Tech Labs	1		
19	Union Signal & Switches	8		

Annexure-C: Details of Infrastructure

Sl. No	Infrastructure	Area in Sq. mts	Remarks
1	Class Rooms (Five Nos.)	103 Sq.Mts Each	
2	Laboratories		

	Digital& Microprocessor Lab	185.15 sq. mt.	
	Communication Lab	133.63 sq. mt	
	Basic/Power Electronics Lab.	185.15 sq. mt	
	Computer/Advanced Microprocessor Lab.	175.15 sq. mt	
	VHDL/DSP Lab.	175.15 sq. mt	
<b>3</b>	Dept. Library		<b>No. of Titles : 375</b> <b>No. of Volumes: 1450</b>
<b>4</b>	Head of the Dept Room.	33.35 Sq.m.	
<b>5</b>	Dept. Office	---	
<b>6</b>	Faculty Room-1	120.18 Sq.m	
<b>7</b>	Faculty Room-2	49.00 Sq.m.	
<b>8</b>	Faculty Room-3	41.37Sq.m.	
<b>9</b>	Instruction Room for Lab	50.4 Sq.m	
	<b>TOTAL</b>	1251.53 Sq.m	

Annexure-D: List of Major Equipments/Computers / Software

Sl. No	Details of the Equipment / Computers / Software ****	Cost in Rs per Item	Qty	Total Cost in Rs.
1	Servers Compaq ML 310 P-IV 2.2 GHz 18.2 GB scsci HDD	81,500.00	2	1,63,000.00

2	Desktop HP Compaq PIV PIV, 3 GHz, 40 GB HDD, 512 MB RAM,Samsung colour Monitor Synchmaster 793MB	61,200.00	3	1,83,825.00
3	IBM ThinkCentre PIV, 2.66 GHz, 256 MB RAM,40 GB HDD, Samsung colour Monitor black	45,900.00	4	1,83,600.00
4	HP Vectra VL 420 PIV , 1.7 GHz ,20GB HDD, 128 MB RAM, Samtron Colour Monitor	49,500.00	3	1,48,500.00
5	Desktop Compaq EVO 380 PIV 1.8 GHz, 40 GB HDD, 256 MB RAM , Samsung Colour monitor	49,771.00	3	1,49,313.00
6	HCL Busy Bee Intel PIV 256MB RAM,40GB HDD,17"Colour Monitor	23,846.15	20	4,76,923.00
7	Diskless Nodes Unicorp	28,900.00	12	3,46,800.00
8	Lap Top IBM Thinkpad Intel Celeron,	52,864.00	1	52,864.00
9	ISDN Trainer -TWO PIV CPU's @1.8Ghz,80 GB HDD (baracuda) D845GVSR mother board	1,44,000.00	2	2,88,000.00
10	Intel Celeron 1.1 GHZ, 20 GB HDD , 810 mother board, 128 MB RAM, 40 GB HDD	17,000.00	2	34,000.00
11	Pc-BRIOBAX 450 MHz, 8 GB HDD, FDD 1.44 MB 64 MB RAM	1,17,800.00	2	2,35,600.00
12	Workstations Intel R , 2.4 GHz,2 GB RAM,160GB HDD,With 19" LCD Monitor	1,18,000.00	12	14,16,000.00
13	HP-Compaq Pentium -D 3.4 GHz,2 GB RAM,80 GB HDD,With WLAN,17" LCD Monitor With Windows XP	52,000.00	23	11,96,000.00
14	LAPTOP (Compaq Presario) Intel R Celeron,1.46 GHz,256 MB RAM,		1	
15	LAPTOP-PIV HP		1	

### Softwares Available

Sl. No	Details of the Equipment / Computers / Software ****	Cost in Rs per Item	Qty	Total Cost in Rs.
1.	MATLAB Ver 6.0 (10-User)	4,25,000.00	1	4,25,000.00
2.	Model ICAP, circuit simulation	50,000.00	1	50,000.00

3	XILINX Software Foundation Series University Education (Including Model Sim)	42,750.00	1	42,750.00
4.	Novel Netware Ver 5.1 25 users	55,450.00	1	55,450.00
5.	SCO open server(25 Users)	1,21,000.00	1	1,21,000.00
6.	MS office pro 2003	10,5000.00	1	10,5000.00
7.	Norton Anti virus	2,000.00	1	2,000.00
8.	Multisim2001 ckt designing	45,000.00	1	45,000.00
9.	Active VHDL, VHDL editor, state editor, IEEE 1076-93 VHDL simulator Active CAD for XILINX OS, schematic HDL, state editor system level simulator, TTL and CMOS memory modes, interface to PCB layout of 1) CPLD/FPGA VHDL logic synthesis XILINX foundation series base with VHDL(place and route tool) XILINX universal device programmer	5,06,000.00	1	5,06,000.00
10	FPGA advantage for HDL design	2,25,000.00	(Multi-user)	2,25,000.00
11	ISDN trainer	2,89,000.00	1	2,89,000.00
12	MS-back office	24,850.00	1	24,850.00
13	Microsim Design LAB	45,248.00	1	45,248.00
14	Borland C++	10,200.00	1	10,200.00
15	Windows Based Communication Protocol Simulator for ISDN Basics (Multi Sim Design Lab -2001)	45,000.00	1	45,000.00
16	Turbo C++ (Ver.7.0)	3,100.00	1	3,100.00
17	Microwind Package	3,50,000.00	50-User	3,50,000.00
18	IE3D Professional Package	3,55,000.00	Single User	3,55,000.00

**Major Equipments List:**

Sl. No	Details of the Equipment / Computers / Software ****	Cost in Rs per Item	Qty	Total Cost in Rs.
1	LAN-T LAN-Trainer (Bench mark)	1,40,747.00	01	1,40,747.00
2	TK Base Universal VLSI	19,908.36	11	2,18,992.00

	Development Kit(ADM)			
3	TK-95108 CPLD	9,950.00	01	9,950.00
4	TK Base FPGA 2S30	19,694.00	01	19,694.00
5	CPLD TK -9572	9,950.00	01	9,950.00
6	ALS(Kit) 8051	5,606.25	08	44,850.00
7	Super O-Z universal Programmer	22,850.00	02	45,700.00
8	TMS-5416 DSP Starter Kit	28,250.00	02	56,500.00
9	ALS-PIC01 PIC Microcontroller Trainer Kit	4,462.80	05	22,314.00
10	CPLD Kit Devices 9572PC84	12,800.00	01	12,800.00
11	DSP Starter Kit TMS 320C5402	22,950.00	04	91,800.00
12	PGLA 32 Channel Logic Analyzer	1,35,000.00	01	1,35,000.00
13	PIC-ALL Programmer	7,497.00	01	7,497.00
14	LAN-Trainer & DCN Trainer	1,49,855.00	01	1,49,855.00
15	DSP Kit TMS-320C5416	16,511.20	02	33,022.40
16	128 MB RAM	1,000.00	06	6,000.00
17	SCSI HDD(9.1 GB)Compaq Prosignia Server	20,500.00	01	20,500.00
18	Mother Board (P-233 MMX)	4,860.00	06	29,160.00
19	14" Colour Monitor (LG)	7,800.00	06	46,800.00
20	PCI VGA Adapter	750.00	06	4,500.00
21	PCI-Ethernet Card	900.00	06	5,400.00
22	CD-Writer I-Omega	4,900.00	01	4,900.00
23	Hard Disk (40GB)	2,500.00	06	15,000.00
24	ALS-NIFC-05 Logic Controller Interface	887.00	06	5,323.00
25	ALS-NIFC-06A Dual DAC I/F	745.00	06	4,470.00
26	ALS-NIFC-07A 8 Bit ADC I/F	898.00	06	5,388.00
27	ALS-NIFC-09 Keyboard Display I/F	1,011.00	01	1,011.00
28	ALS-NIFC-17 Elevator I/F	655.00	01	655.00
29	ALS-NIFC-01 A Stepper Motor Interface	2217.25	04	8,869.00
30	ALS-NIFC-14 Seven Segment Display	543.00	01	543.00
31	PIC ALS-NIFC-16 LED intensity Controller and Waveform Generator Board	320.00	01	320.00
32	PIC ALS-NIFC-17 Push Button & Switch I/F Board	543.00	01	543.00
33	Analog I/F Board	1011.00	01	1011.00
34	ALS-NIFC-39 Matrix Key board 3X8 I/F	815.00	04	3,400.00
35	ALS-NIFC-38 Multiplexed seven segment Display Board	950.00	03	2,850.00
36	ALS-NIFC-14 A Printer I/F With Cable	1075.00	03	3,075.00
37	ALS-PCI-02 ADC/DAC Digital I/O Card	10975.00	05	54,875.00
38	DES-108, 8Port 10/100 Switch Make-D-Link	2,200.00	02	4,400.00
39	CD-Writer 52 X External I-Omega	5,000.00	01	5,000.00

40	D-Link 16 Port Switch	3,800.00	01	3,800.00
41	Battery 12V 7-Plates 45 AH	990.00	09	8,910.00
42	Air conditioners	26,990.00	02	53,980.00
43	LCD Projector Sharp PG-A10S	1,05,000.00	1	1,05,000.00
44	Topex Camera Flex Vision	35,000.00	1	35,000.00
45	LCD Projector Toshiba	53,555.00	2	1,07,110.00
46	D-Link Fax Model 33.6KBPS (Modem)	3,300.00	1	3,300.00
47	DSP kits TMS320c5416	26,260.00	8	2,10,080.00
48	ALS NIFC -46 multiplexed display and keypad	1,275.00	3	3,825.00
49	ALS NIFC -45 LCD and key board	1,050.00	2	3,100.00
50	ALS NIFC -17 Elevator control i/f	750.00	5	3,750.00
51	ALS NIFC -47 Temperature measurement I/F	4,000.00	2	8,000.00
52	ALS NIFC -53 DC motor i/f	5,950.00	2	11,900.00
53	ALS SDA 51 ME Micro control trainer kit	6,300.00	4	25,200.00
54	ARM Processor 9X	49,900.00	6	2,99,400.00
55	ARM Processor 7X	24,900.00	7	1,74,300.00
56	ARM 7 T Board University Kit	79,900.00	1	79,900.00
57	DSP Processor, TMS320C6713 DSK	1,10,000.00	1	1,10,000.00

**Main Lab**

Sl. No	Details of the Equipment / Computers / Software ****	Cost in Rs per Item	Qty	Total Cost in Rs.
1	Microprocessor Kits	4,573.30	61	2,78,966.00
2	Microcontroller Kits	4,333.00	12	52,000.00
3	IC-Trainers	5,322.00	08	42,577.00
4	IC-Tester	15,933.00	03	47,800.00
5	EPROM Programmers	1,250.00	04	5,000.00
6	EPROM Eraser	1,692.58	03	5,077.74
7	Digital Multimeter	1,183.00	15	17,750.00
8	Oscilloscope	12,390.00	10	1,23,900.00
9	Spectrum Analyzer	6,26,288.00	01	6,26,288.00
10	Digital Storage Oscilloscopes	1,23,424.00	05	6,17,120.00
11	Oscilloscopes	21,503.00	14	3,01,044.00
12	Communication Package	9,100.00	02	18,200.00
13	Power supplies	4,718.80	27	1,27,409.00
14	Function Generators	6,724.70	31	2,08,466.00
15	Microwave Benches	1,03,454.20	05	5,17,271.00
16	Digital Communication Trainer Kit	42,000.00	04	1,68,000.00
17	Fiber Optic Kits	48,910.60	05	2,44,553.00
18	Radar Demonstration Kits		01	
19	Digital Transmission Analyzer	3,29,688.00	01	3,29,688.00
20	Power Meters	2,945.75	04	11,783.00
21	Pattern Generator	3,405.00	03	10,215.00
22	Pulse Generator	4,350.00	02	8,700.00
23	Decade Resistance Box	1,336.60	30	40,100.00
24	Decade Inductance Box	2,926.60	30	87,800.00
25	Decade Capacitance Box	2,172.70	22	47,800.00

26	Digital Multimeter	1,183.40	15	17,750.00
27	DB Step Attenuator	2,400.00	01	2,400.00
28	Frequency Counter	9,666.60	03	29,000.00
29	LCR Meter	17,350.00	02	30,700.00
30	Thyristor Modules (Inverter & Converter)	131.90	44	5,803.60
31	Rectifier Diode Modules	1,030.00	20	20,600.00
32	Fast Recovery Diode Modules	1,240.00	10	12,400.00
33	Three Phase Firing Circuit Modules	9,750.00	03	29,250.00
34	Single Phase Firing Circuit Modules	3,600.00	08	20,800.00
35	Single Phase Transistorized PWM Converter	9,740.00	01	9,740.00
36	Power MOSFET Chopper	6,420.00	01	6,420.00
37	Power MOSFET Module	1,090.00	10	10,900.00
38	RC Network Module	360.00	10	3,600.00
39	LC Commutation Module	1,475.00	03	4,425.00
40	GTO Thyristorised Chopper	12,680.00	01	12,680.00
41	GTO Thyristor Module	3,598.33	06	21,590.00
42	PID Controller	18,270.00	01	18,270.00
43	DC Position Control unit	14,200.00	01	14,200.00
44	Power Scopes	22,301.16	06	1,33,807.00
45	Differential Module	8,291.00	01	8,291.00
46	Dimmer Starts	3,800.00	13	49,404.00
47	Transformers	3,235.18	16	51,763.00
48	Power Supplies	342.36	15	5,135.40
49	Oscilloscopes	10,329.40	10	1,03,294.00
50	Decade Capacitance Box	2,529.41	17	43,000.00
51	Digital Multimeter	1,375.00	16	22,000.00
52	Decade Inductance Box	1,446.66	15	21,700.00
53	Decade Resistance Box	2,666.66	15	40,000.00
54	Analog Multimeters	3,312.50	16	53,000.00
55	Rheostats (All ranges)	648.00	50	32,400.00
56	Ammeters	607.14	28	17,000.00
57	LCR Meter	7,850.00	02	15,700.00
58	Digital Multimeter	1,113.00	10	11,130.00

Annexure-E: List of Major Projects

Sl. No	Details of the Projects	Sponsoring Agency	Coordinator	Total Cost in Rs.
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1	Telephone Attending System (1994-1997)	Karnataka State Council for Science & Technology, Bangalore. (KSCST)	Prof. Suresh Radder	6,500.00
2	VGA to PAL (1994-1995)		Prof. Ravindra.M.	3,100.00
3	Sonic Path Finder (1995-1996)		Dr. M. Suresh.Nayak	2,500.00
4	Arecanut Peeler (1996-1997)		Dr. M. Suresh.Nayak	5,600.00
5	Voice Identification using TMS 320C50 (1998-1999)		Prof. M.V.Latte	8,000.00
6	A Microprocessor & Microcontroller development system (2002-2003)		Prof. Rekha V.Dundur	2,000.00
7	Multiple Device Switching / Status Monitoring (2003-2004)		Prof. V.K. Parvati Prof. S.S. Kerur	2,500.00